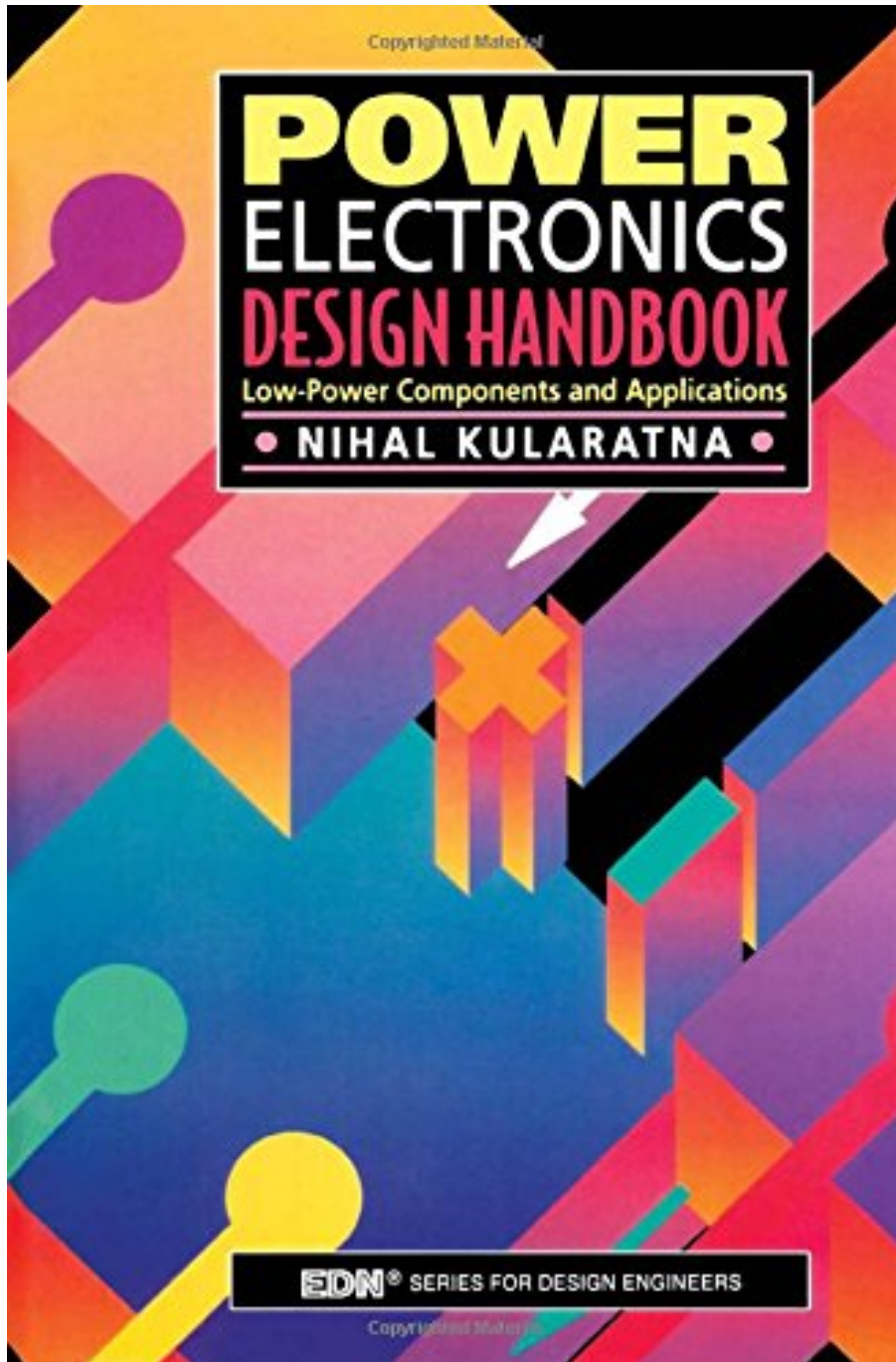


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"...offers excellent tutorial-style coverage of basic component properties in addition to a breadth of information on the basic functional blocks of power systems. A good quick reference source." - PCIM

'The author, an expert in the field, integrates component and system theory with practical applications, particularly energy-saving, low-power applications. Many chapters contain sections on future developments. There are also references for further research and more in-depth technical reading.' - Popular Electronics

'This excellent, well-written text is a must for power quality engineers, technicians, marketers, manufacturers and designers. This is a very comprehensive power quality handbook.' - Power Quality Assurance

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About the Author

Nihal Kularatna is the author of Power Electronics Design Handbook. He is an electronics engineer with over 30 years of experience in professional and research environments. He is a Fellow of the IEE (London), a Senior Member of IEEE (USA) and an honors graduate from University of Peradeniya, Sri Lanka. Presently, he is a Senior Lecturer in the Department of Engineering, the University of Waikato, New Zealand. He worked at the Arthur C. Clarke Institute for Modern Technologies (ACCIMT) in Sri Lanka as a

Research and Development Engineer until 1990 when he reached Principal Researcher Engineer status. He was then appointed as CEO of ACCIMT in 2000. From 2002 to 2005 he was a Senior Lecturer at the Department of Electrical and Electronic Engineering, University of Auckland. He is currently active in research in transient propagation and power conditioning area in power electronics, embedded processing applications for power electronics, and smart sensor systems. He has authored five books and is currently working on his sixth. His hobby is gardening cacti and succulents.

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Power Electronics Design Handbook covers the basics of power electronics theory and components while emphasizing modern low-power components and applications. Coverage includes power semiconductors, converters, power supplies, batteries, protection systems, and power ICs.

One of the unique features of the Power Electronics Design Handbook is the integration of component and system theory with practical applications, particularly energy-saving low-power applications. Many chapters also include a section that looks forward to future developments in that area. References for further information or more in-depth technical reading are also included.

Nihal Kularatna is a principal research engineer with the Arthur C. Clarke Foundation in Sri Lanka. He is also the author of Modern Electronic Test and Measuring Instruments, published by the Institute of Electrical Engineers.

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